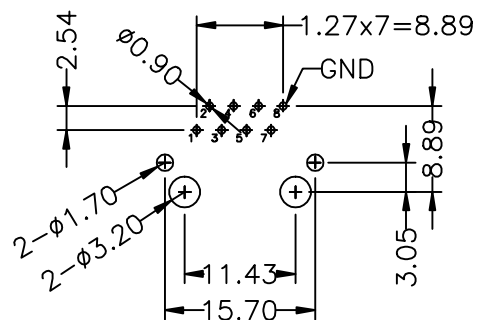
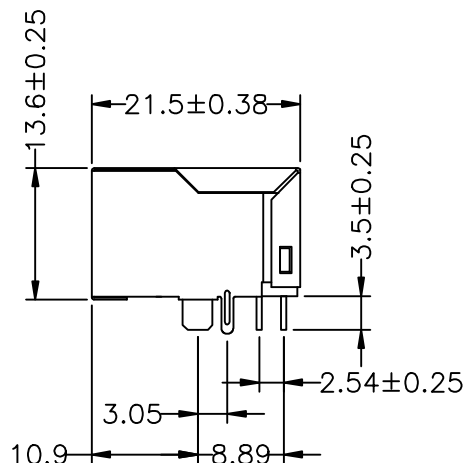
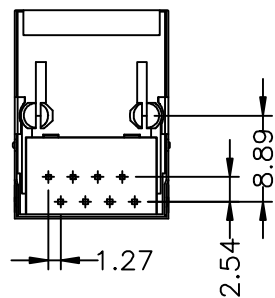
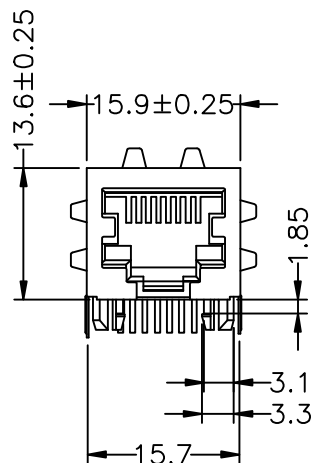
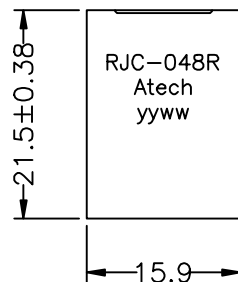


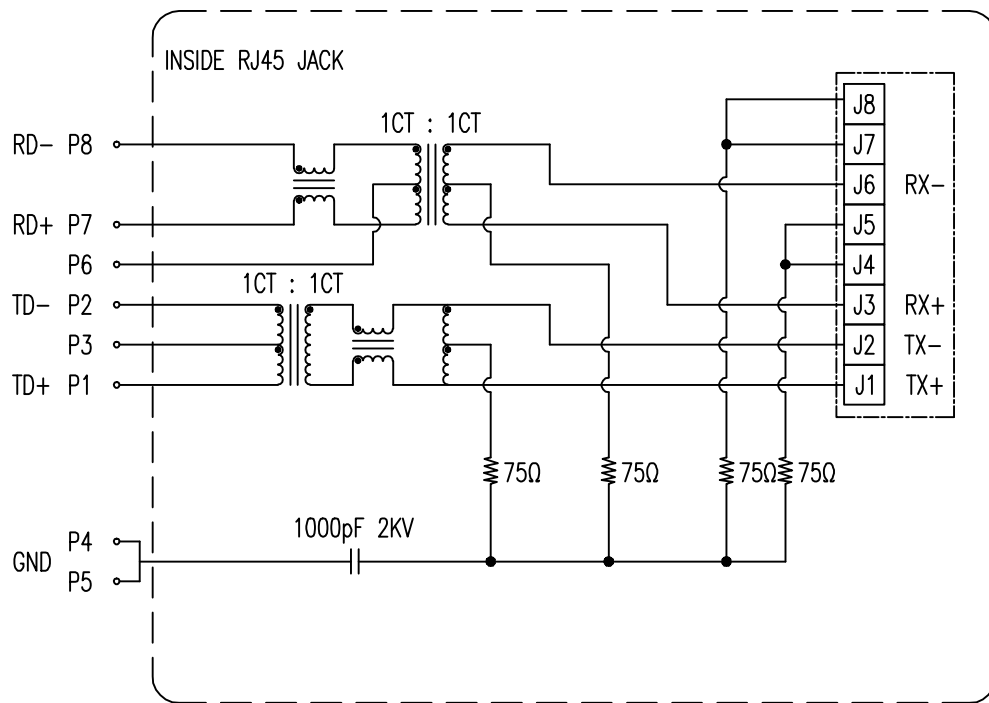


PCB LAYOUT

ELECTRICAL CHARACTERISTICS :

1. TURNS RATIO : TX/RX = 1CT : 1CT $\pm$ 3%.
2. OCL (@100KHz / 0.1Vrms, WITH 8mA DC BIAS) : 350uH MIN.
3. INSERTION LOSS (@0.1-100MHz) = -1.0 dB MAX.
4. RETURN LOSS : 1~30MHz = -18dB MIN.
30~60MHz = -16dB MIN.
60~80MHz = -12dB MIN.
5. CROSS TALK (dB MIN.) : 0.5-1MHz 10MHz 30MHz 50MHz 100MHz
-50 -50 -40 -35 -30
6. CMRR (dB MIN.) : 0.3-1MHz 10MHz 30MHz 60MHz 100MHz
-50 -47 -42 -37 -30
7. HI-POT : 1500 Vrms.
8. RoHS COMPLIANT

						AOEM. Atech		TITLE RJ45 1*1 10/100 BASE-T WITH MAGNETIC MODULE DWG. NO. RJC-048R	
						UNITS: M/M		SAFETY	
						DATE 02/07/12		P/N:	
REVISIONS						02/07/12		DRAW SHIRLEY	
NO:	DESCRIPTION	DATE	BY	CHK	APPD			SHEET 1 OF 2	
	RELEASE	02/07/2012	SHIRLEY	BELLY	BELLY				



								TITLE RJ45 1*1 10/100 BASE-T WITH MAGUETIC MODULE	
								DWG. NO. RJC-048R	
	RELEASE	02/07/2012	SHIRLEY	BELLY	BELLY	UNITS: M/M	SAFETY		SHEET 2 of 2
NO:	DESCRIPTION	DATE	BY	CHK	APPD	DATE	P/N:	DRAW	
REVISIONS						02/07/12			SHIRLEY