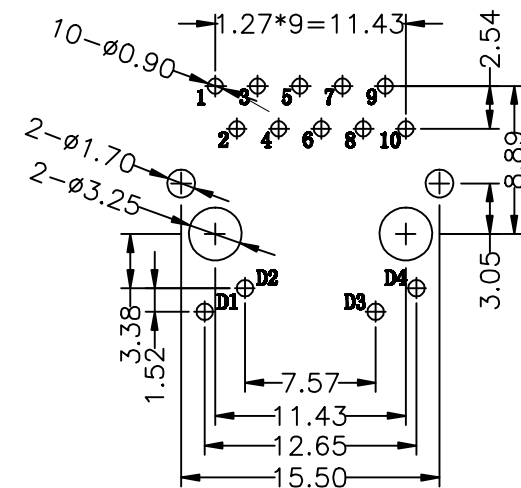
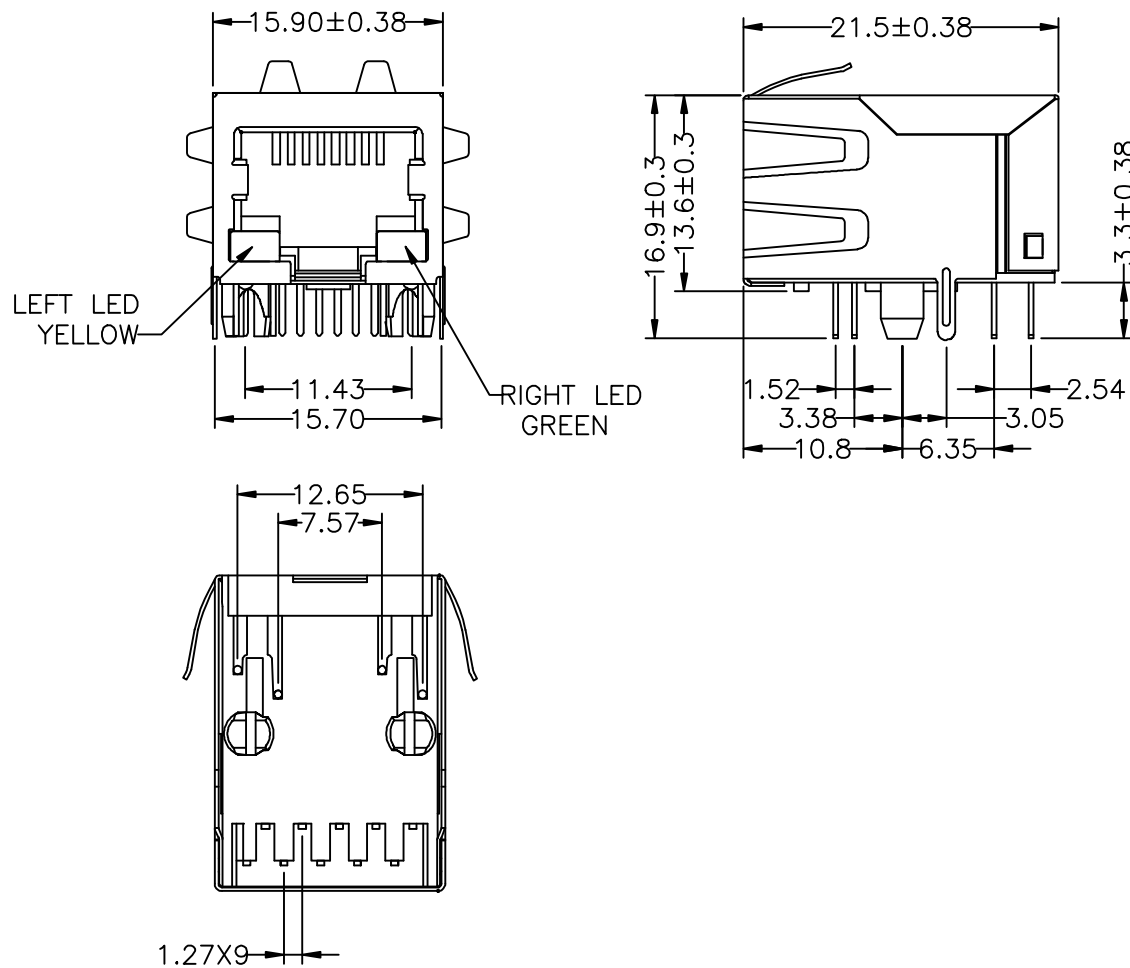




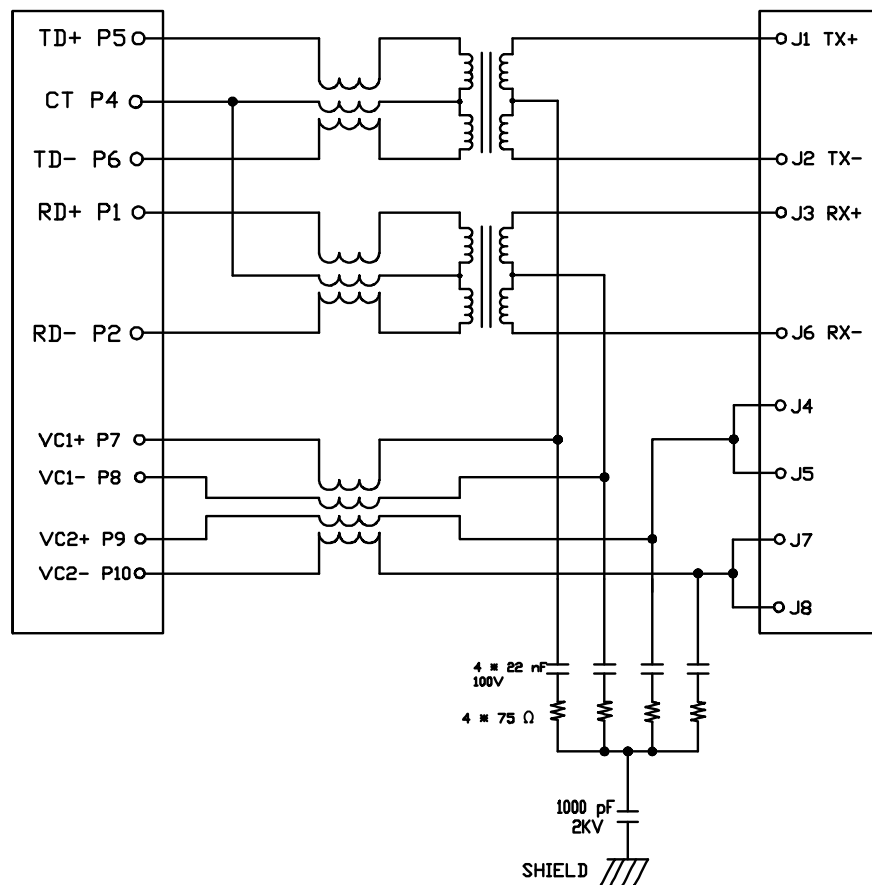
PCB LAYOUT

TOLERANCE : $xx.x0 \pm 0.25$
 $0.xx \pm 0.05$
 UNIT : mm

								TITLE RJ45 1*1 W/PoE TRANSFORMER 10/100 BASE	
								DWG. NO. RJC-041R	
	RELEASE	06/10/2011	SHIRLEY	BETTY	BETTY	UNITS: M/M	SAFETY		
NO:	DESCRIPTION	DATE	BY	CHK	APPD	DATE 06/10/11	P/N:	SHEET 1 of 2	
REVISIONS								DRAW Shirley	

LAN CHIP SIDE

RJ CABLE SIDE



ELECTRICAL CHARACTERISTICS :

1. TURNS RATIO : TX/RX = 1CT : 1CT $\pm$ 3%.
2. HI-POT : 1500 Vrms.
3. DCR : J1-J2 / J3-J6 = 1.2 Ω MAX.
4. OCL (@100KHz / 0.1Vrms, WITH 8mA DC BIAS) : 350uH MIN.
5. INSERTION LOSS (@0.3-100MHz) = -1.0 dB MAX.
6. RETURN LOSS : 1~30MHz = -20dB MIN.
30~60MHz = -15dB MIN.
60~80MHz = -10dB MIN.
7. CROSS TALK (@1-100MHz) = -40dB TYP.
8. CMRR (@1-100MHz) : -40dB TYP.
9. WORKING CURRENT FOR PoE HAS 350mA
10. RoHS COMPLIANT

								TITLE RJ45 1*1 W/PoE TRANSFORMER 10/100 BASE	
								DWG. NO. RJC-041R	
	RELEASE	06/10/2011	SHIRLEY	BETTY	BETTY	UNITS: M/M	SAFETY		SHEET 2 OF 2
NO:	DESCRIPTION	DATE	BY	CHK	APPD	DATE	P/N:	DRAW	
REVISIONS						06/10/11			Shirley